	Application No.	Applicant(s)
Notice of Allowability	10/849,035	FUKUHARA, KAZUYA
	Examiner	Art Unit
	Samuel A. Gebremariam	2811
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT F of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in to b) or other appropriate communi RIGHTS. This application is sub	nis application. If not included cation will be mailed in due course. THIS
1. This communication is responsive to 8/16/05.		
2. The allowed claim(s) is/are <u>5-7</u> .		
3. Acknowledgment is made of a claim for foreign priority u a) All b) Some* c) None of the: 1. Certified copies of the priority documents hav 2. Certified copies of the priority documents hav 3. Copies of the certified copies of the priority do International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE MAILING DATE noted below. Failure to timely comply will result in ABANDON! THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be subminformal PATENT APPLICATION (PTO-152) which gives the including changes required by the Notice of Draftsper (a) including changes required by the Notice of Draftsper (b) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner Paper No./Mail Date (b) Including indicia such as the application number (see 37 CFR each sheet. Replacement sheet(s) should be labeled as such in 6. DEPOSIT OF and/or INFORMATION about the department of the paper in the paper in the department of the paper in the paper in the paper in the department of the paper in	re been received. re been received in Application occuments have been received in application occuments have been received in a first of this communication to file a ment of this application. mitted. Note the attached EXAM res reason(s) why the oath or dust be submitted. reson's Patent Drawing Review (No In this national stage application from the reply complying with the requirements INER'S AMENDMENT or NOTICE OF eclaration is deficient. PTO-948) attached the Office action of drawings in the front (not the back) of 1.121(d).
Attachment(s) 1. ☐ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Sum Paper No./Ma /08), 7. ☑ Examiner's Ar	mal Patent Application (PTO-152) Imary (PTO-413), ail Date nendment/Comment atement of Reasons for Allowance

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DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Arthur J. Steiner on 10/27/05.

The application has been amended as follows: Claims 8-12 are cancelled.

Claim 5, delete lines 3-19 and insert--- a semiconductor substrate having a first surface and a second surface opposite to each other, the semiconductor substrate having a source electrode and a gate electrode provided on the first surface and a drain electrode provided on the second surface; a source terminal layer disposed on the first surface and joined to the source electrode; a gate terminal layer disposed on the first surface and joined to the gate electrode; and a drain terminal layer disposed on the second surface and joined to the drain electrode; wherein the source terminal layer and the gate terminal layer are respectively disposed on the first surface with such sizes as to fall within the area of the first surface, and the drain terminal layer is disposed with such a size as to fall within the area of the second surface, and a circuit board, wherein the power MOSFET is packaged in such a manner that the respective first and second

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surfaces of the semiconductor substrate in the power MOSFET are substantially normal

Allowance

2. Claims 5-7 are allowed.

to the circuit board.---.

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Samuel A. Gebremariam whose telephone number is (571)-272-1653. The examiner can normally be reached on 8:00am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

SAG October 31, 2005 Steven Sole

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